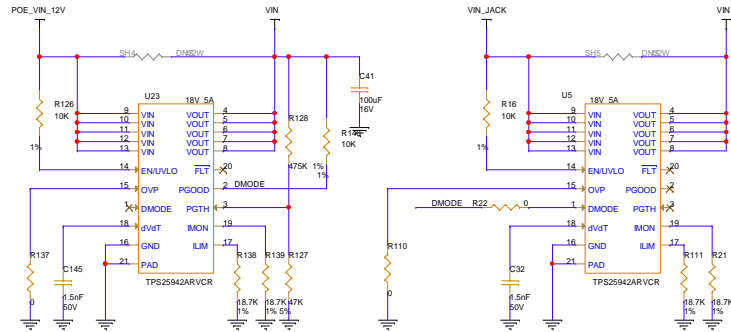
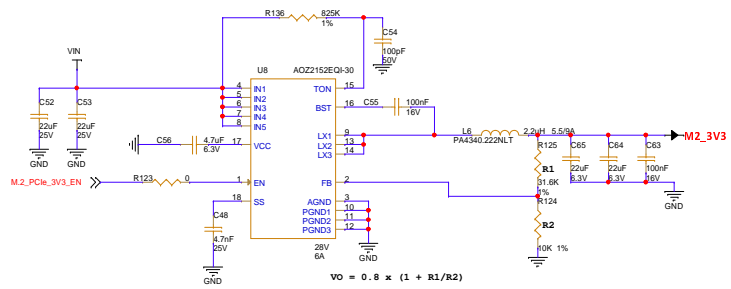


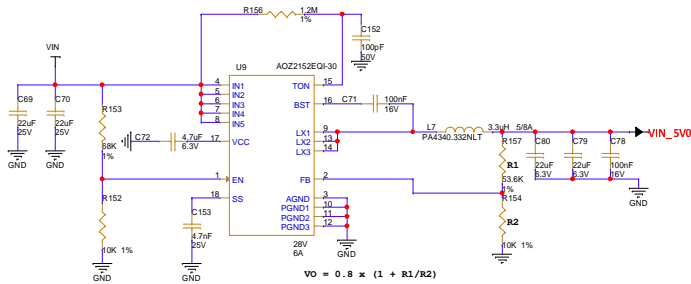
SH4, SH5 bypass U40 and U41 when Vin is a single source (Adapter or POE)

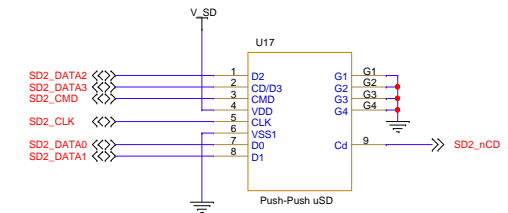
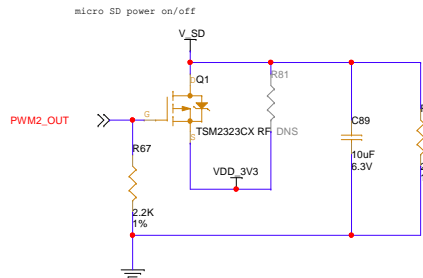


3V3/6A powers the M.2 and Mini-PCIe modules

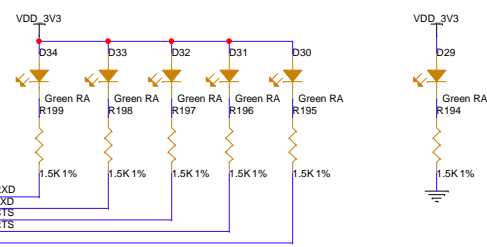
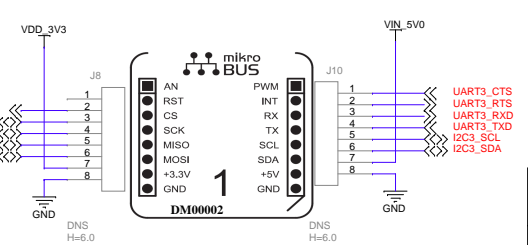
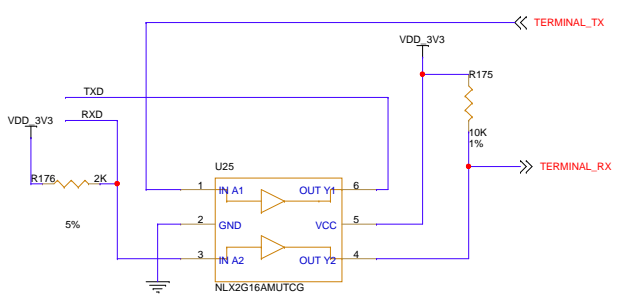
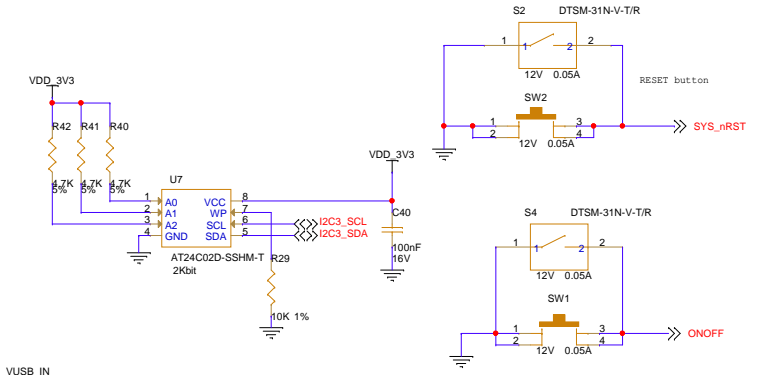
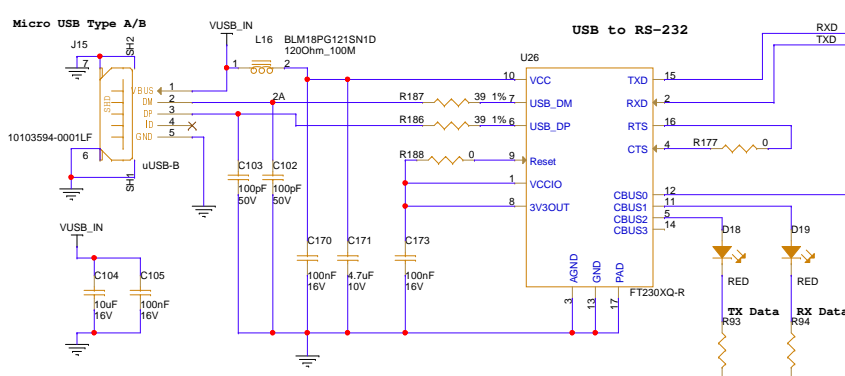


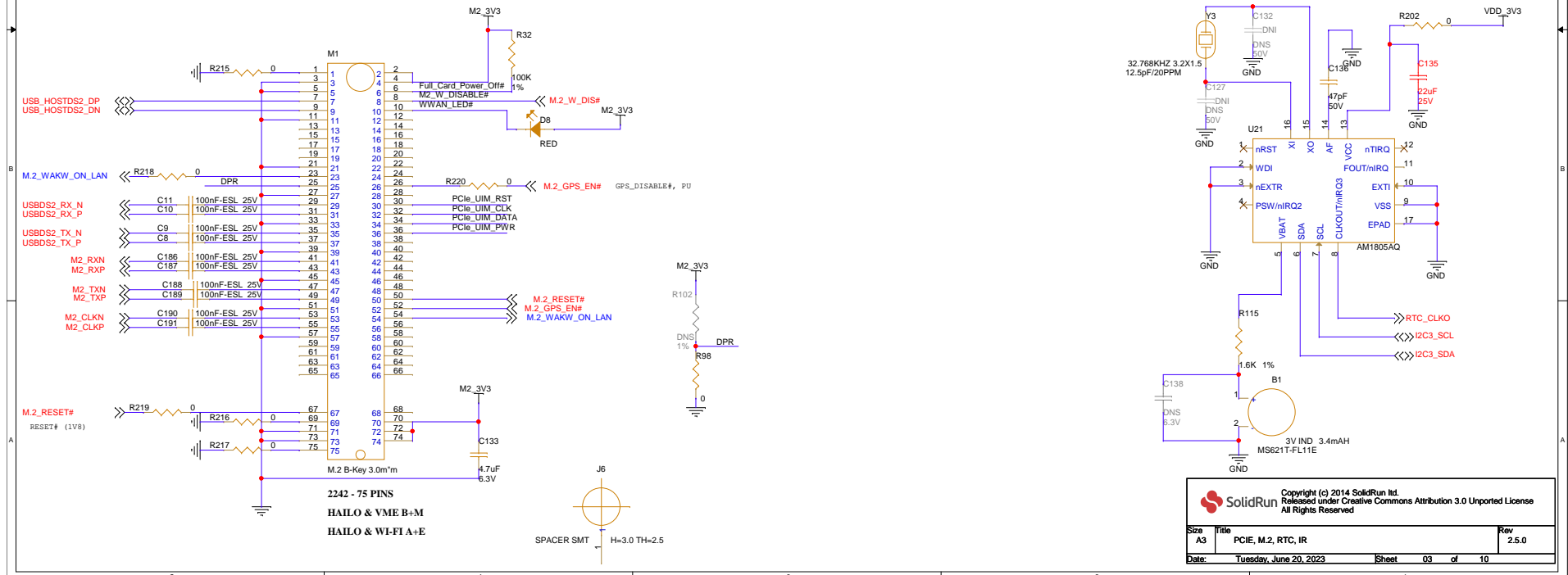
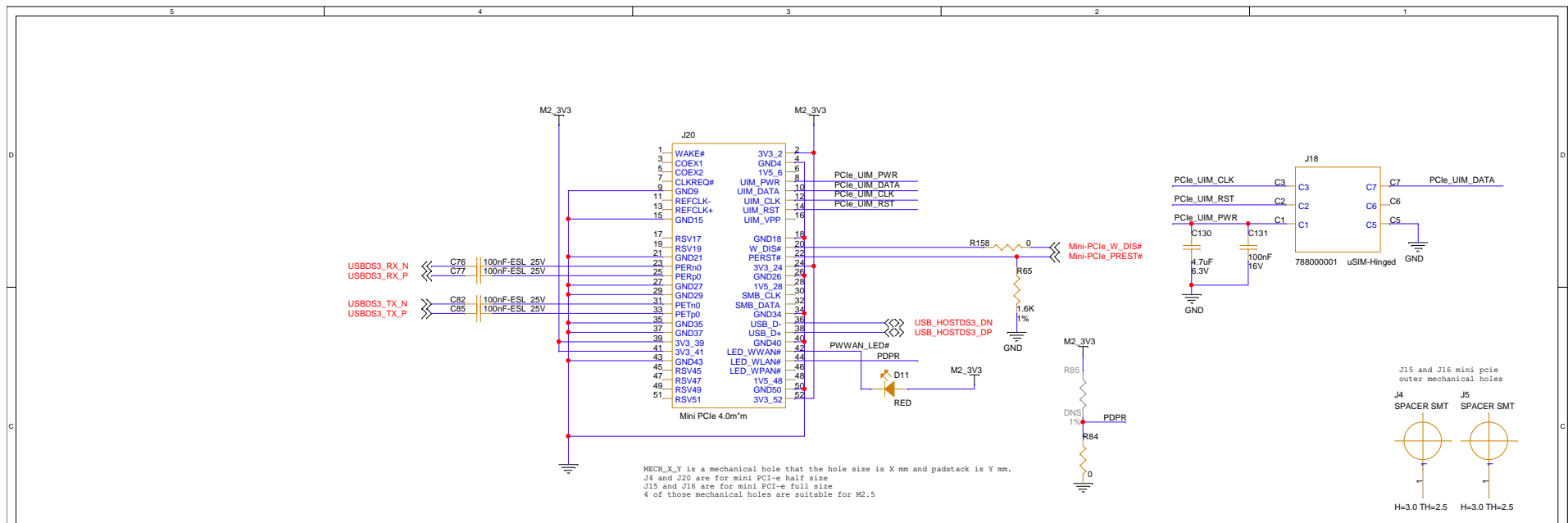
5V/6A powers the SOM and USB power out

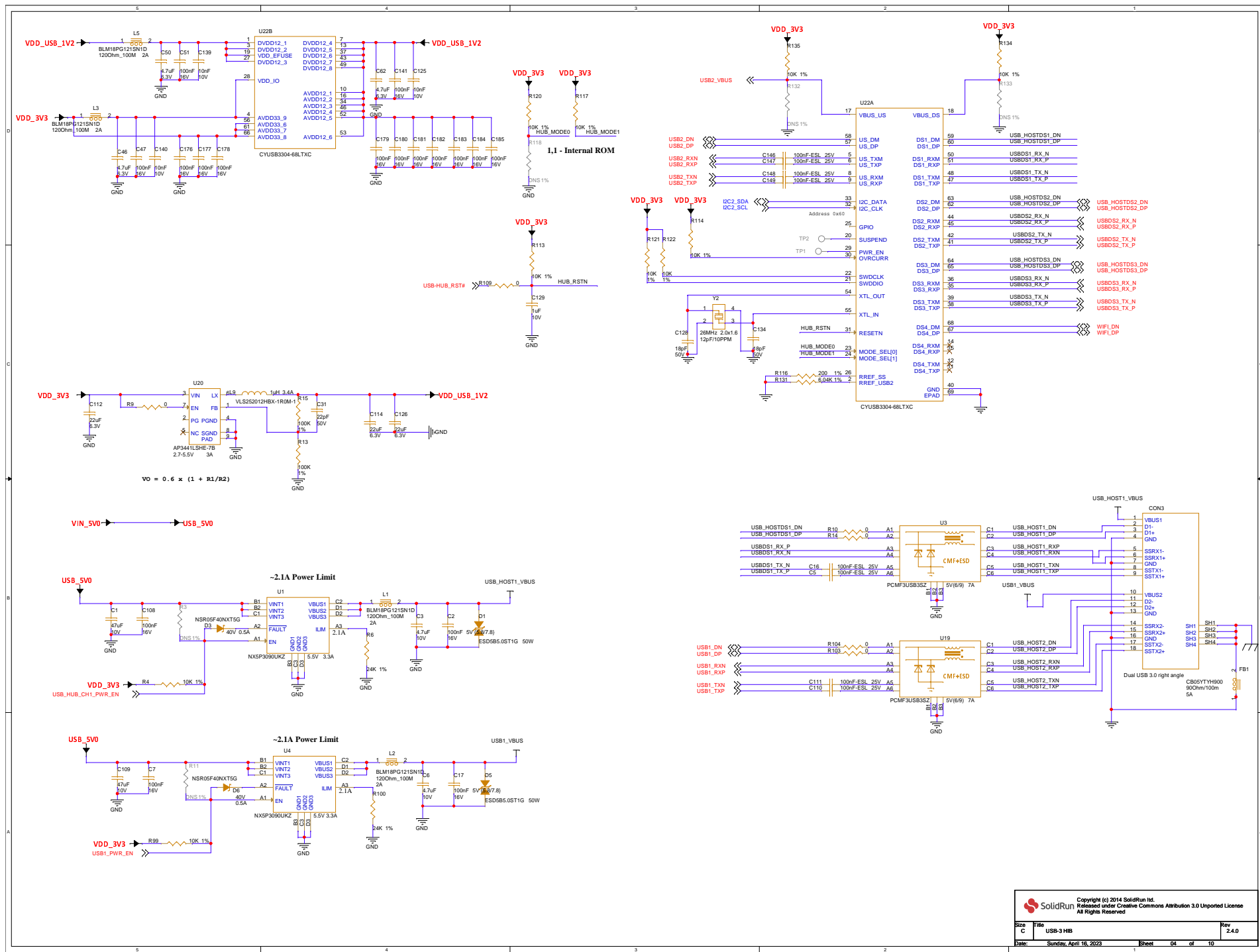


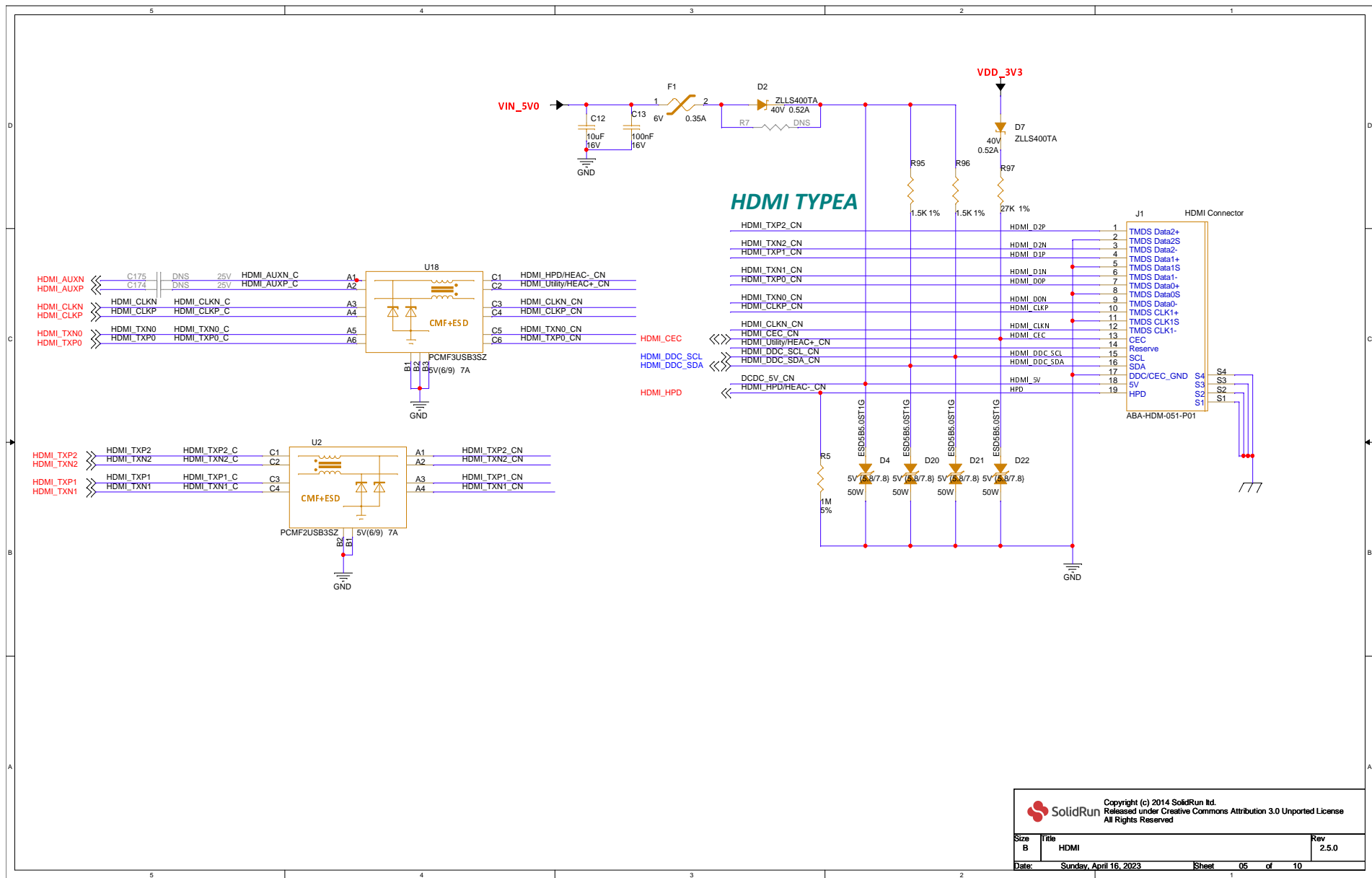


DEBUG/Control PORT

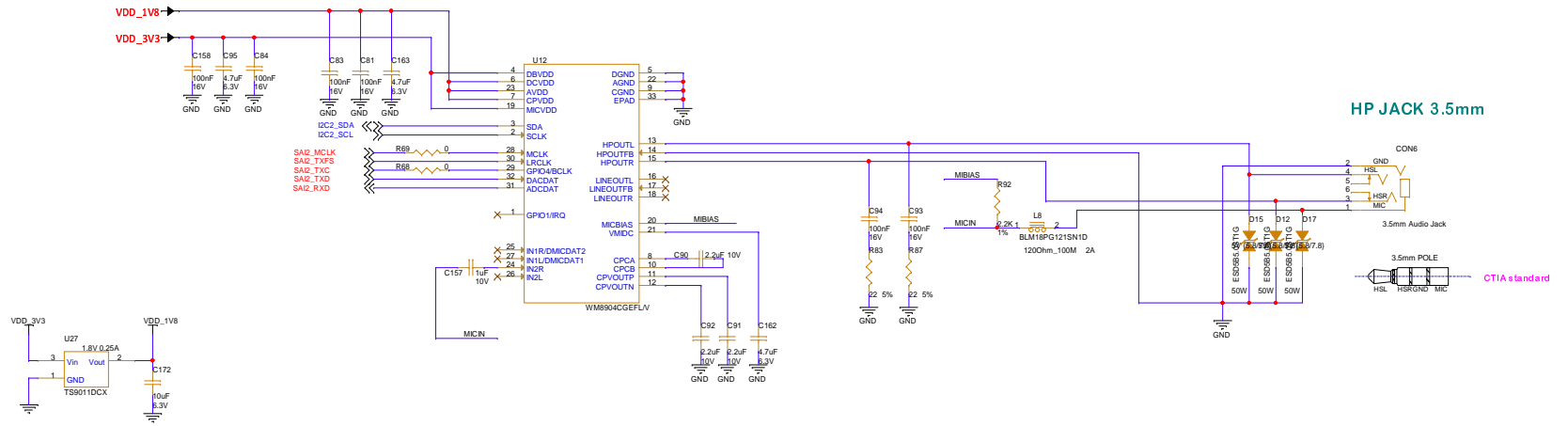


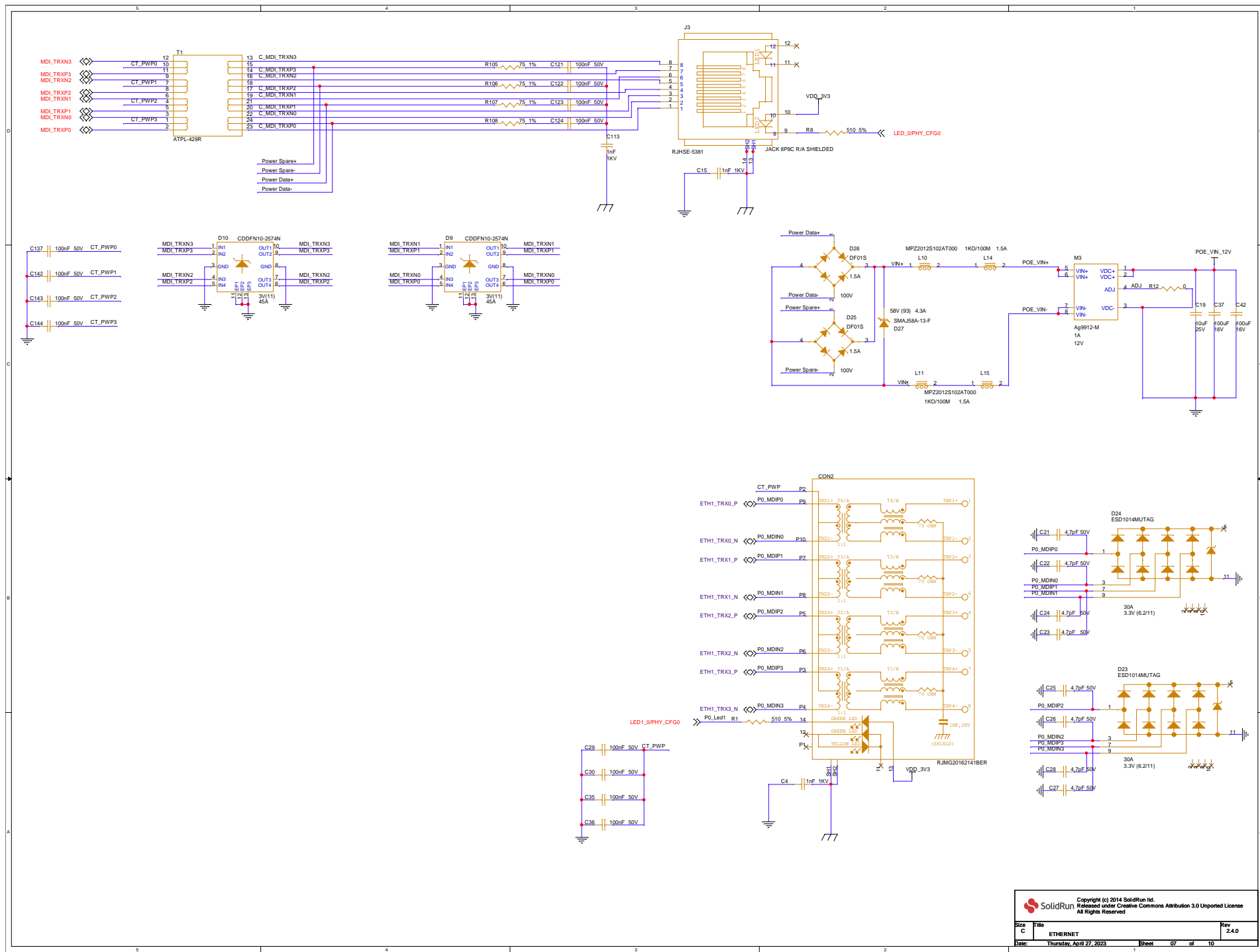


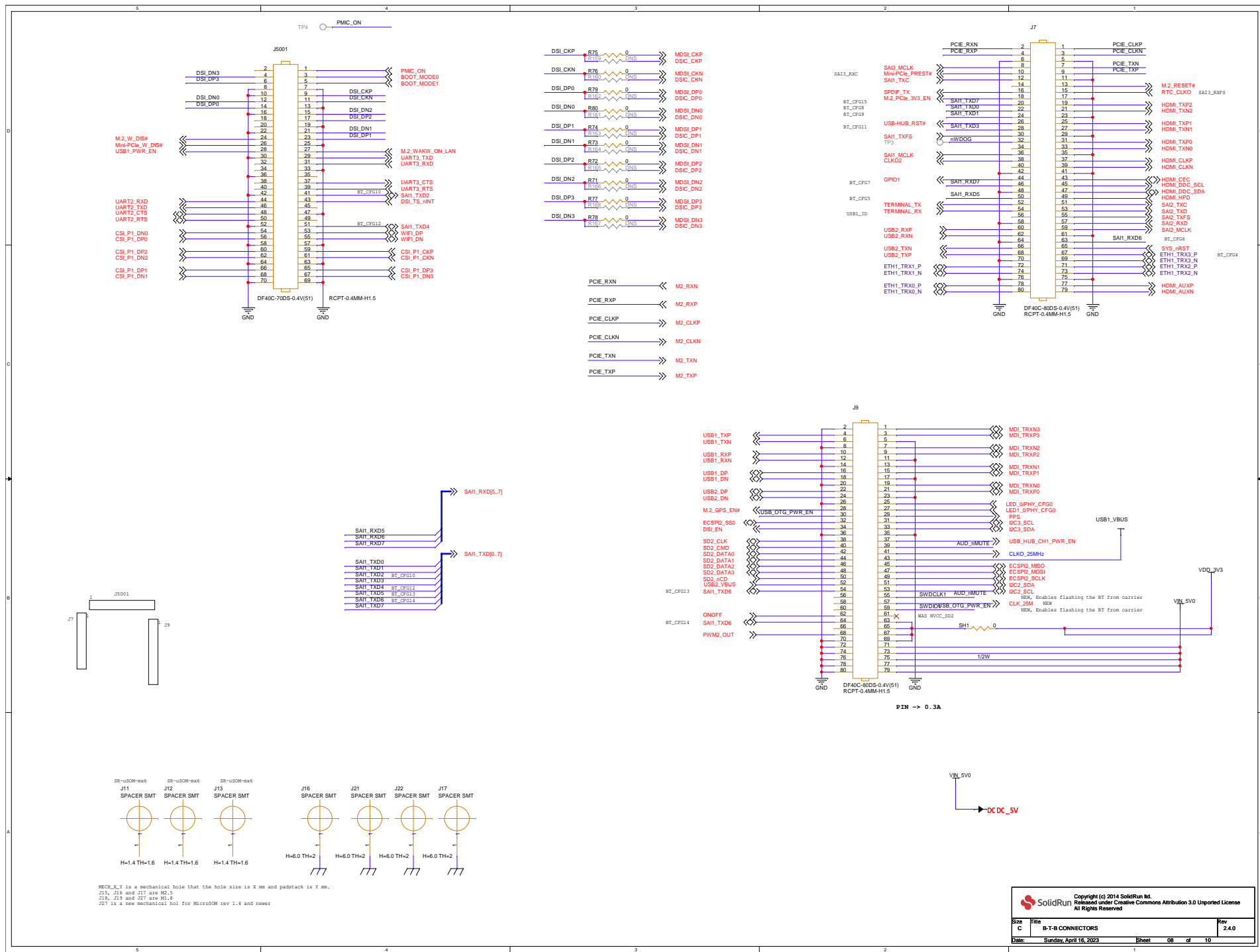




Audio DAC

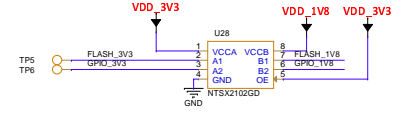
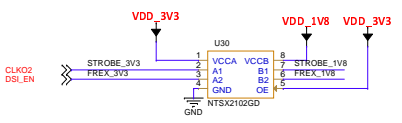
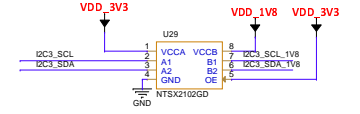
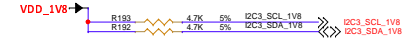
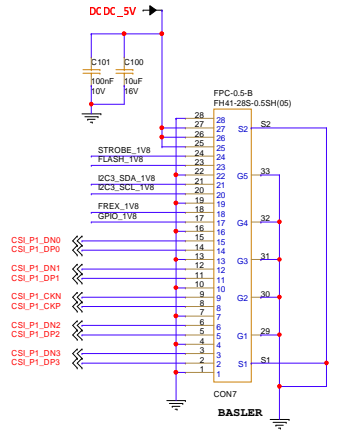
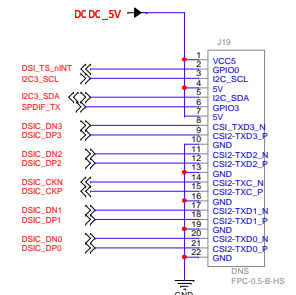
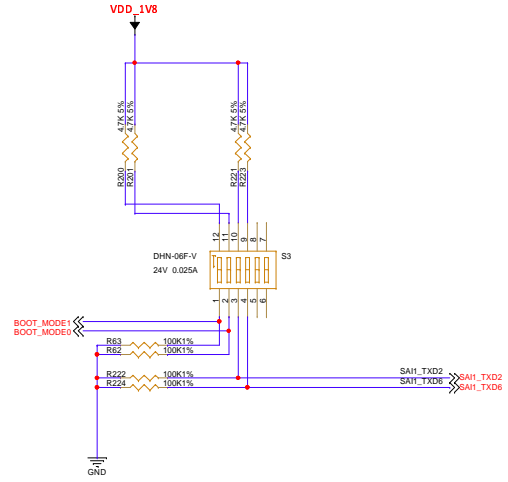
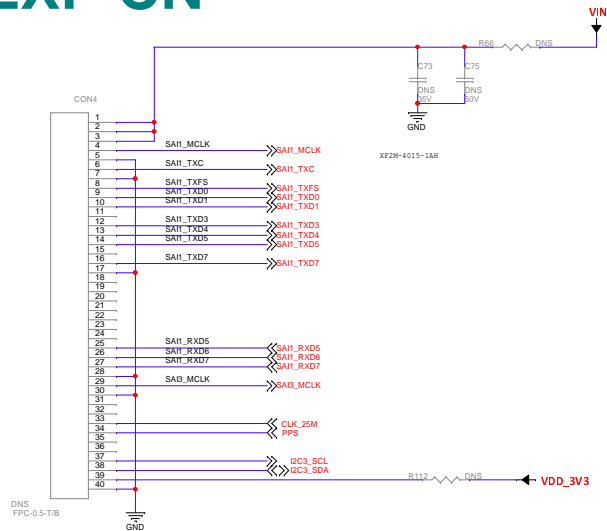


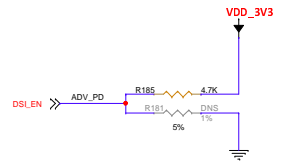
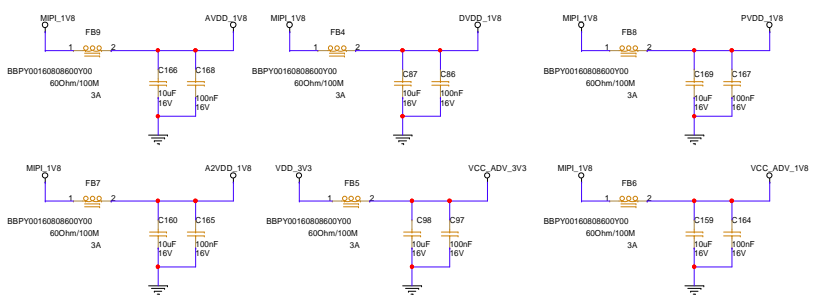
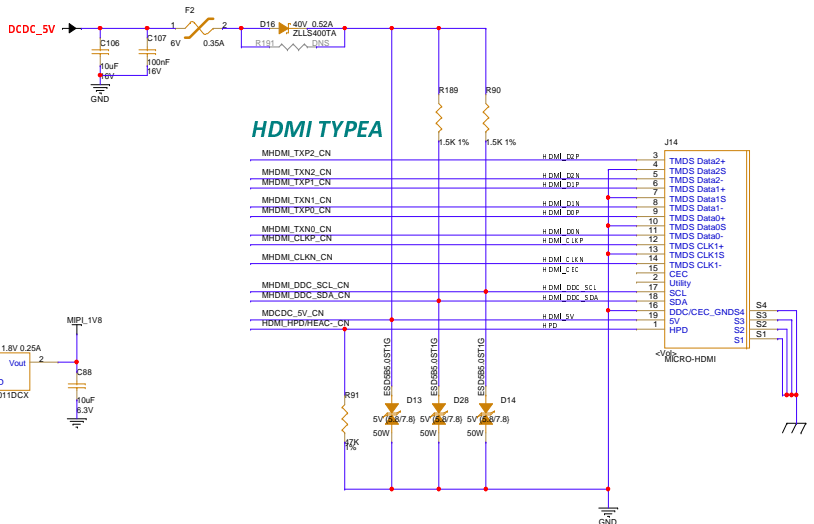
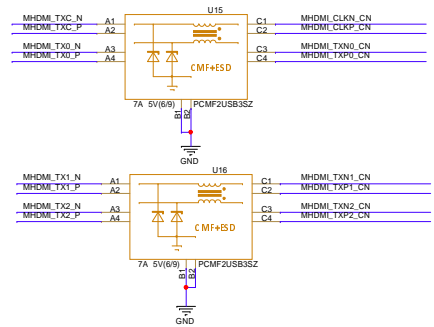
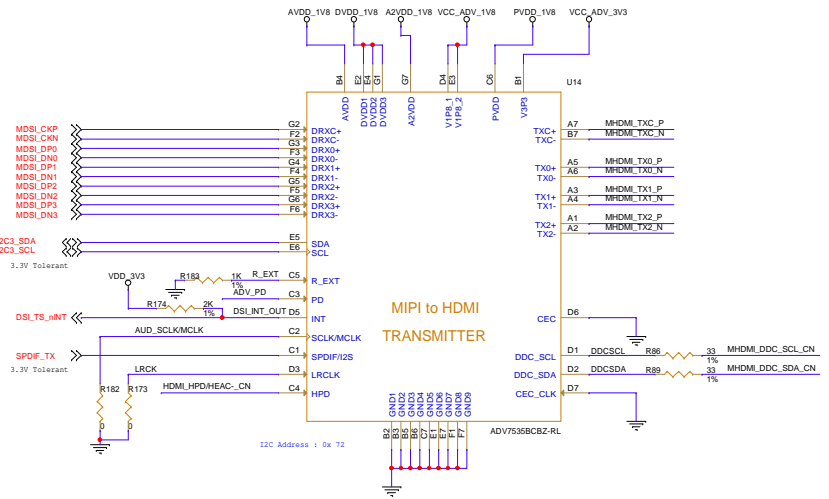




MECH_X_Y is a mechanical hole that the hole size is X mm and padstack is Y mm.
 J15, J16 and J17 are M2.5
 J19, J25 and J27 are M1.6
 J27 is a new mechanical hole for MicroSD rev 1.4 and newer

EXP CN





Rev. 1.0	Prototype Version
Rev. 2.0	<ol style="list-style-type: none"> 1. Change R257 from 0 Ohm to 100K 2. Adding C297 a 4.7uF on USB Debug 3. Change J35 Routing 4. R23 is assembled instead of R25 to support standalone USB HUB mode 5. Connecting VIN_5V0 to DCDC_5V 6. Changing the Power load switch to TPS25942A to support reverse power blocking 7. Changing J5001, J7 and J9 routing to support the new IMX-8 SOM 8. Change the Audio CODEC to WM8904 to support IN/OUT 9. Change Y4 from 27MHz to 25MHz
Rev. 2.1	<ol style="list-style-type: none"> 1. Adding R373 and R374 0 Ohm to pull OVP input down for U40 and U41. <p>Taking U37 and U38 out of the BOM and change revision to 2.1.3</p>
Rev. 2.2	New features product
Rev. 2.3	<ol style="list-style-type: none"> 1. Fixing I2C routing on U14 2. Fixing symbols errors on layout - U8, U9 and CON7 3. Changing OC1 position to enable POE module assembly on print side
Rev. 2.3.1	1. R185 is not assembled
Rev. 2.3.2	1. On HBP ripple adding U27 and C172 to the assembly list to enable VDD_1V8
Rev. 2.4	<ol style="list-style-type: none"> 1. Adding R200 and R201 to enable 1.8V pullup on MODE0 and MODE1 signals (IMX8M-Mini ans IMX8M-Plus) 2. Connecting a reset signal to the Intel NIC. (CLKO_25MHz, J9-41) 3. Changed the B-t-B connector names to J5001, J7 and J9 according to the SOM. 4. Changed J9 to J10 to support item 3.
Rev. 2.5	<ol style="list-style-type: none"> 1. Changed C135 to 100uF in 1210 package 2. Added R202 (1K OHM) between V3V and the RTC power to support slower slew rate on power down. 3. Added C174 and C175 to support IMX8-Plus HDMI 4. Added C176, C177 and C178 to filter USB HUB AVDD power 5. Added C179 - C185 to filter the HUB VDD_USB_1V2 power 6. Placing U18 on CS and changing some of the signals routing to support that 7. Fixing the reset signal to the Intel NIC. (CLKO_25MHz, J9-41) and R35 is not assembled to set reset to "0".
Rev. 2.5.3	<ol style="list-style-type: none"> 1. Adding POE modem to the electrical BOM 2. Changing J6 from MCH0006 to MCH0008 to support the correct height of the M.2 Module 3. Adding pull up R185 to enable the MIPI DSI to HDMI chip when using IMX8M Plus 4. All HBP options support Boot voltage of 1.8V
Rev. 2.6	<ol style="list-style-type: none"> 1. Adding option to use the PCIe on the M.2 2. Adding PB to the ON/OFF signal