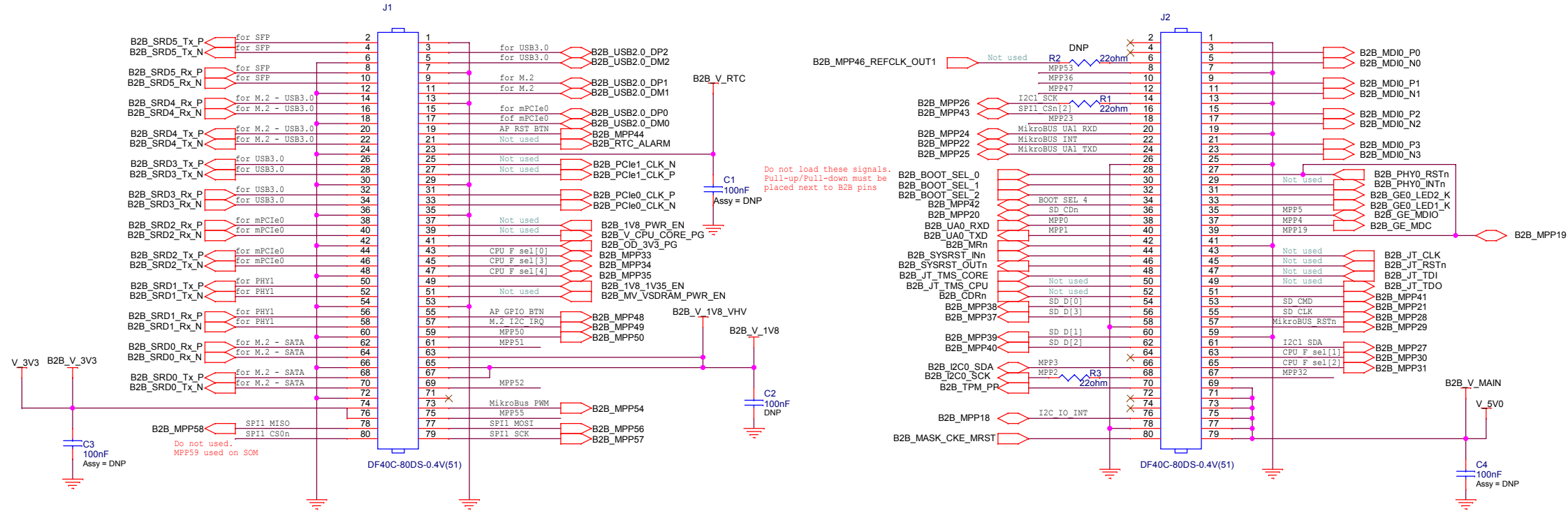


To Extract BOM:

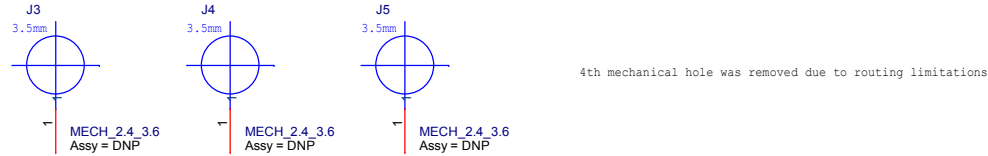
Item\tQuantity\tAssemblyOption\tPart\tPCB Footprint\tDescription\tDataSheet\tManufacturer\tManufacturer P/N\tSolidRun P/N\tReference
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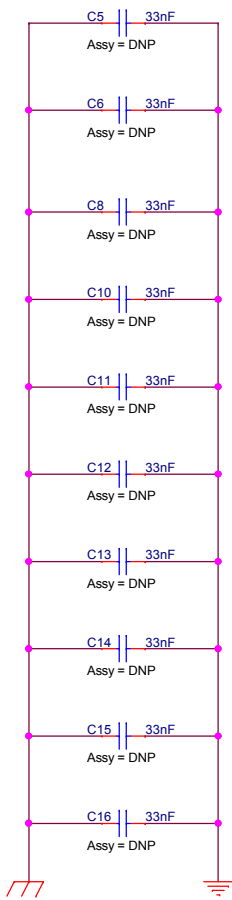
Item\tQuantity\tAssemblyOption\tValue\tDescription\tManufacturer Name\tManufacturer P/N\tSolidRun P/N\tPCB Footprint\tReference
{Item}\t{Quantity}\t{Assy}\t{Value}\t{DESCRIPTION}\t{Manufacturer Name}\t{Manufacturer P/N}\t{SolidRun P/N}\t{PCB Footprint}\t{Reference}



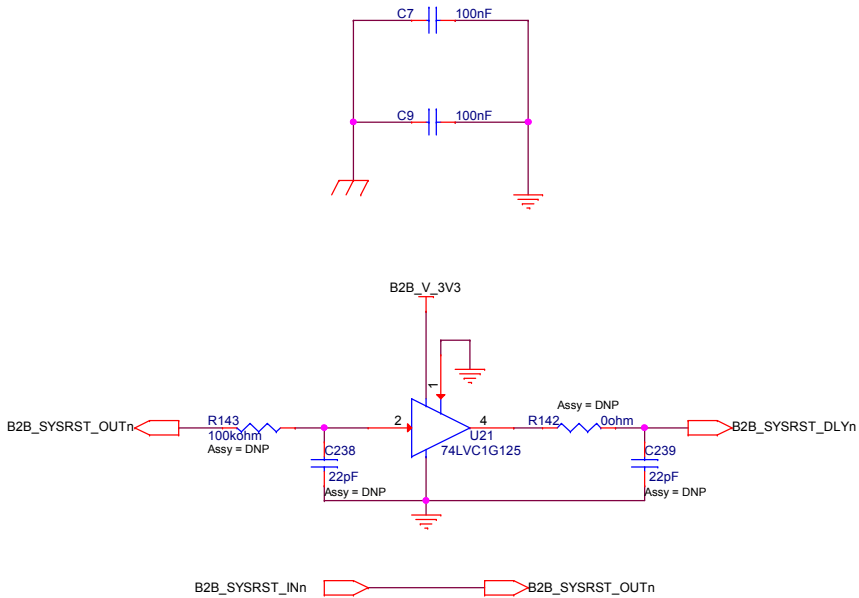
Four mechanical holes for the MicroSoM



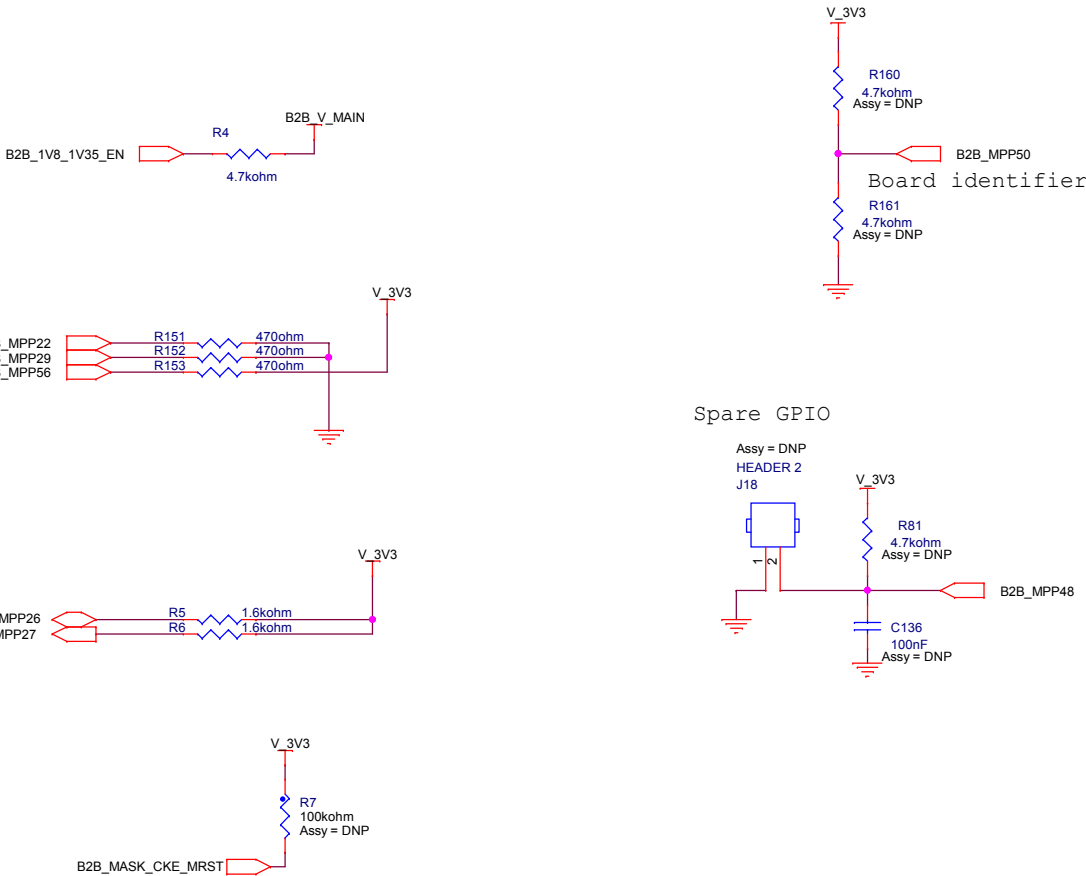
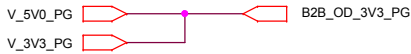
Bypass capacitors between GNDC and GND

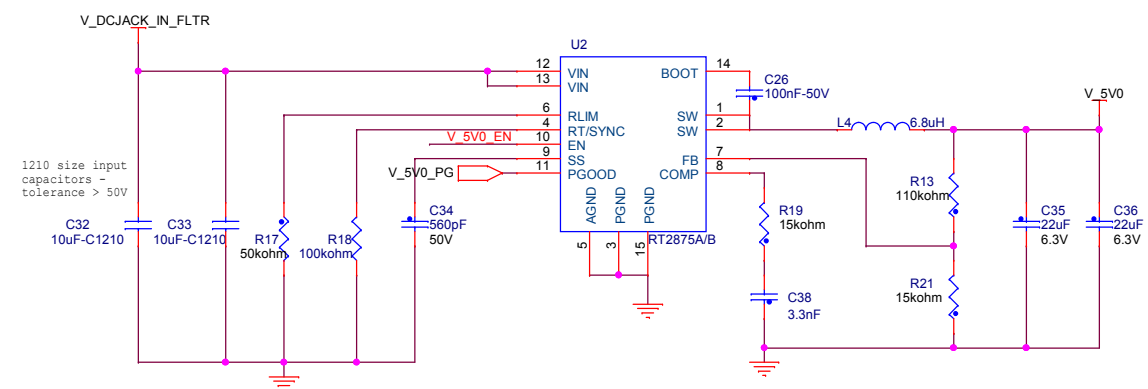
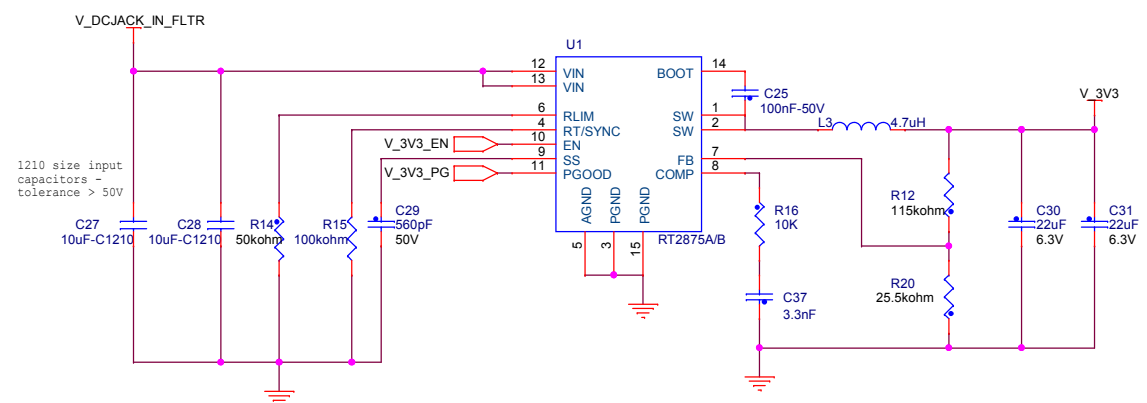
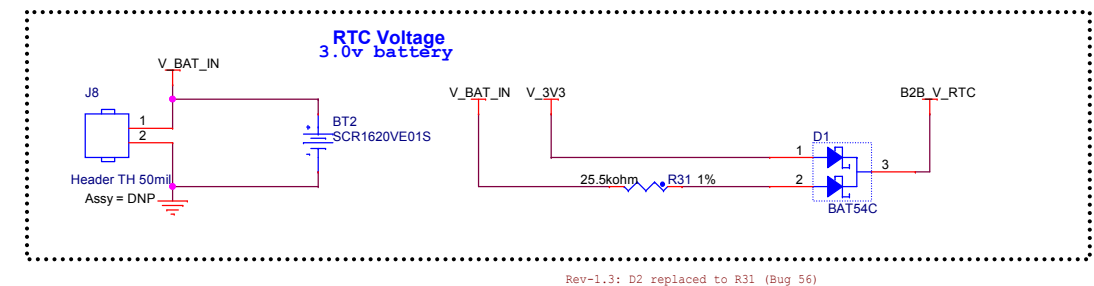
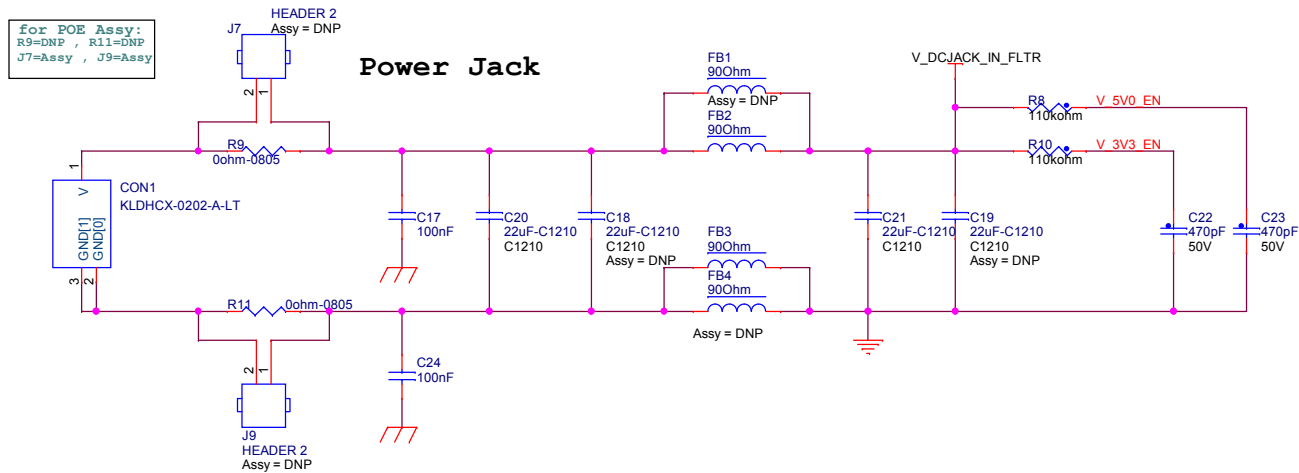


Bypass capacitors for MDIO passing underneath GNDC

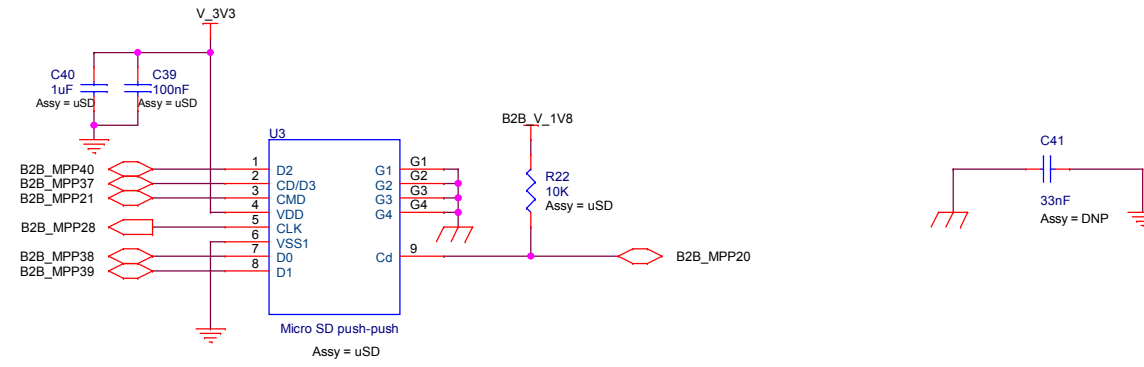


Carrier DC-DC PGOOD Signals will enable CPU DC-DC on uSoM. Signal is pulled up on uSoM.



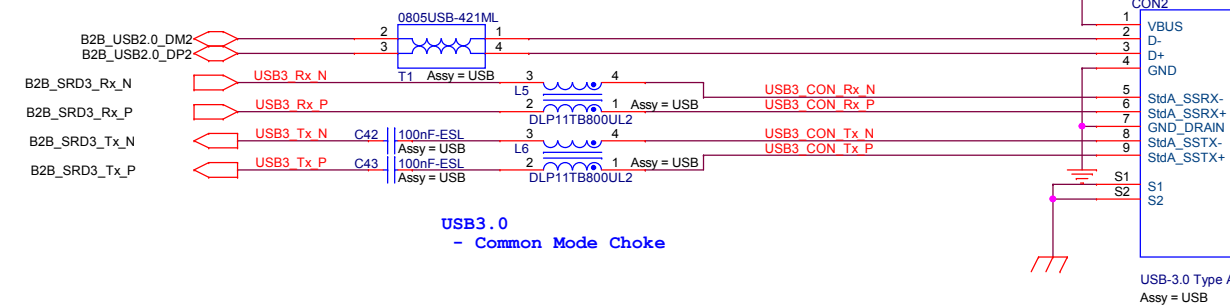


uSD



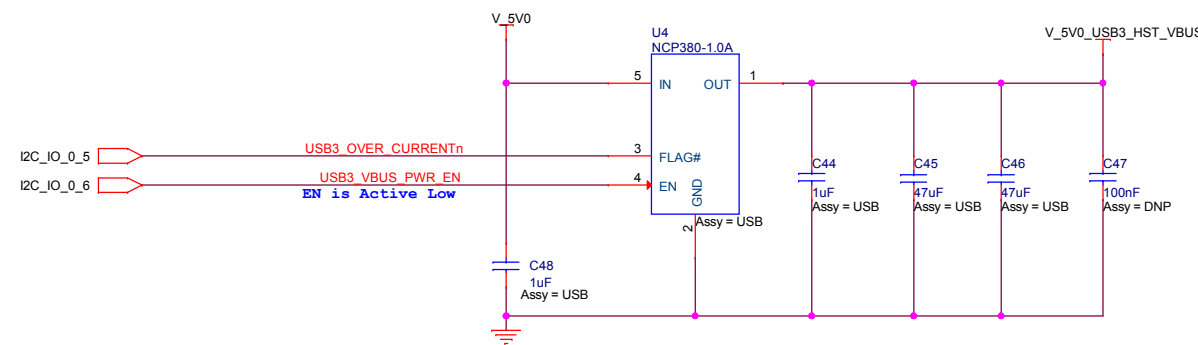
USB2.0
- Common Mode Choke

Connector side



5v/1A Power out
to USB3 device
V_5V0_USB3_HST_VBUS

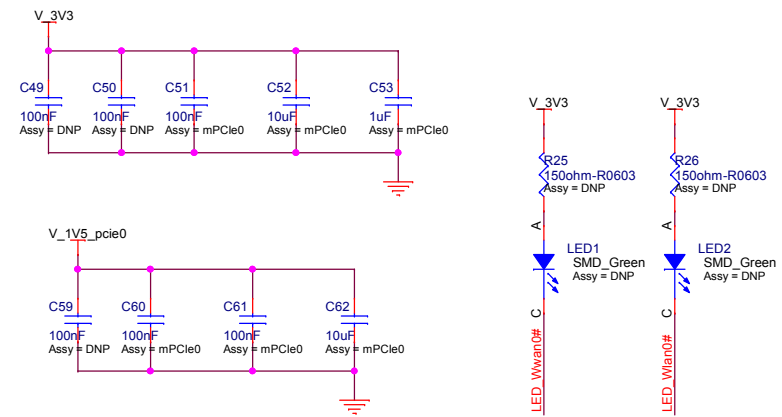
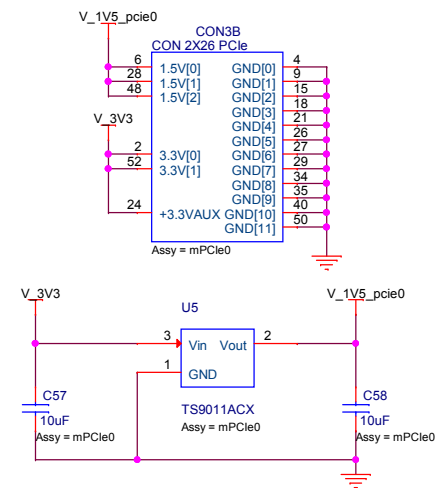
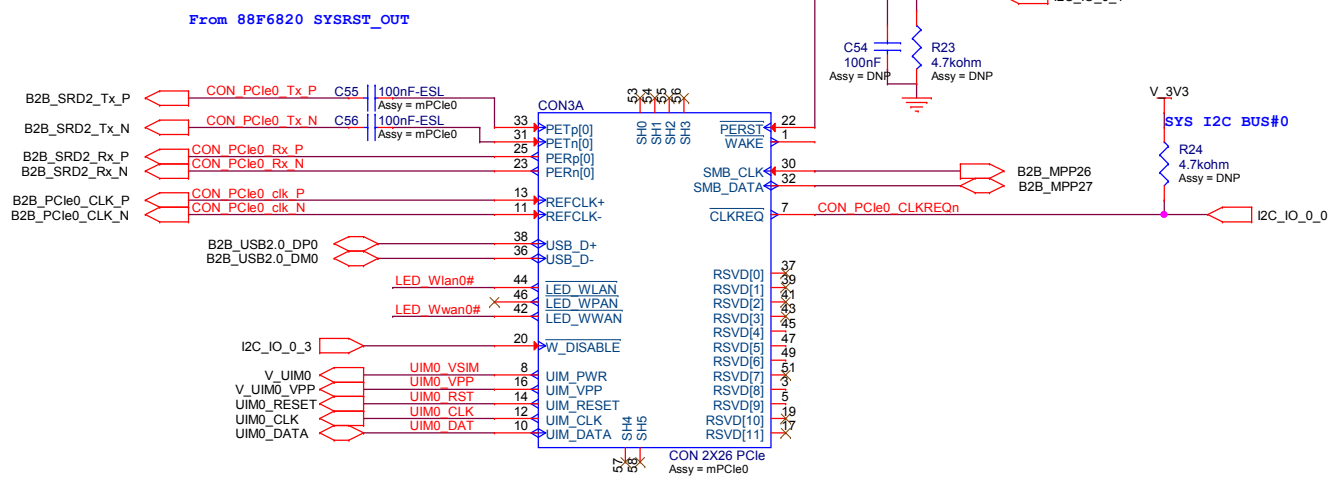
MV USB3.0 HOST



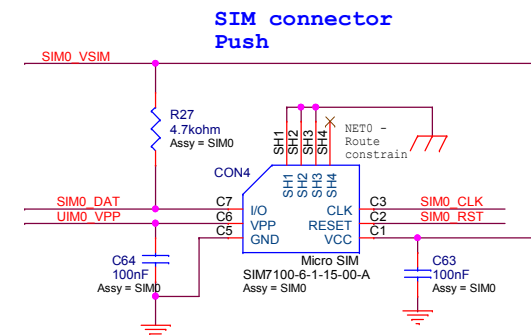
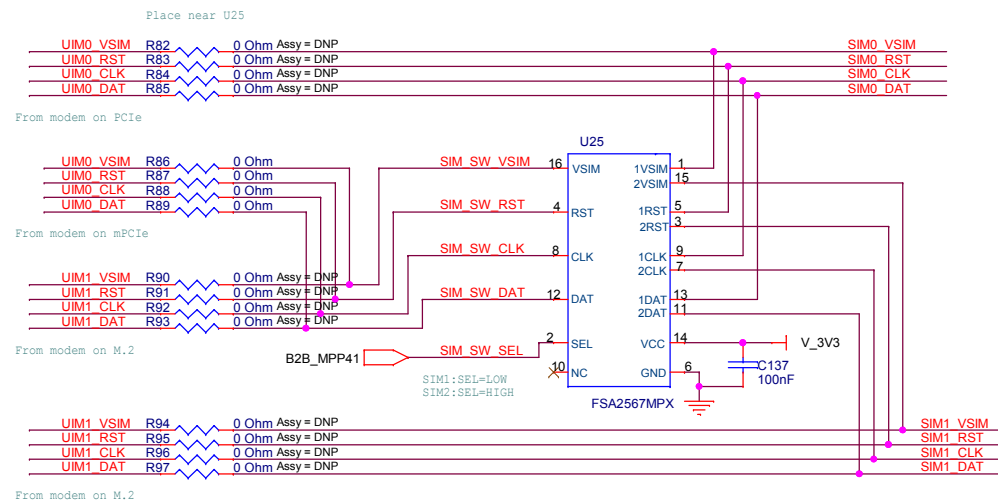
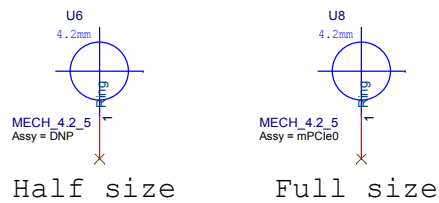
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Size	C	Title	uSD + USB3.0	Rev	1.0
Date:	Monday, July 15, 2019	Sheet	4	of	8

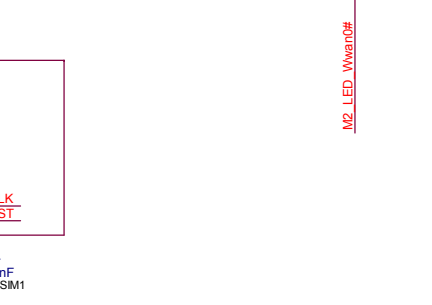
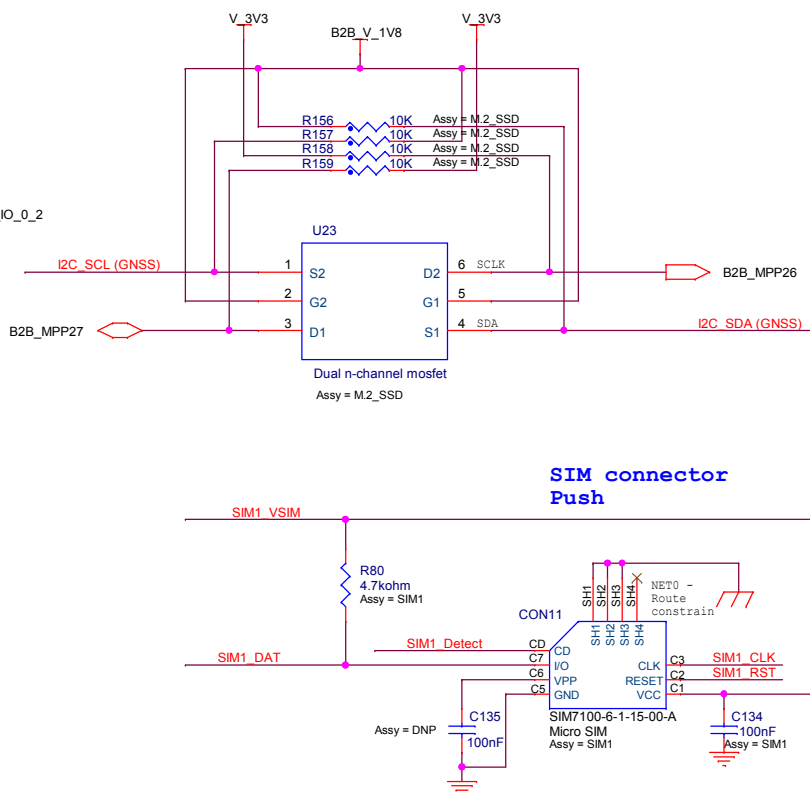
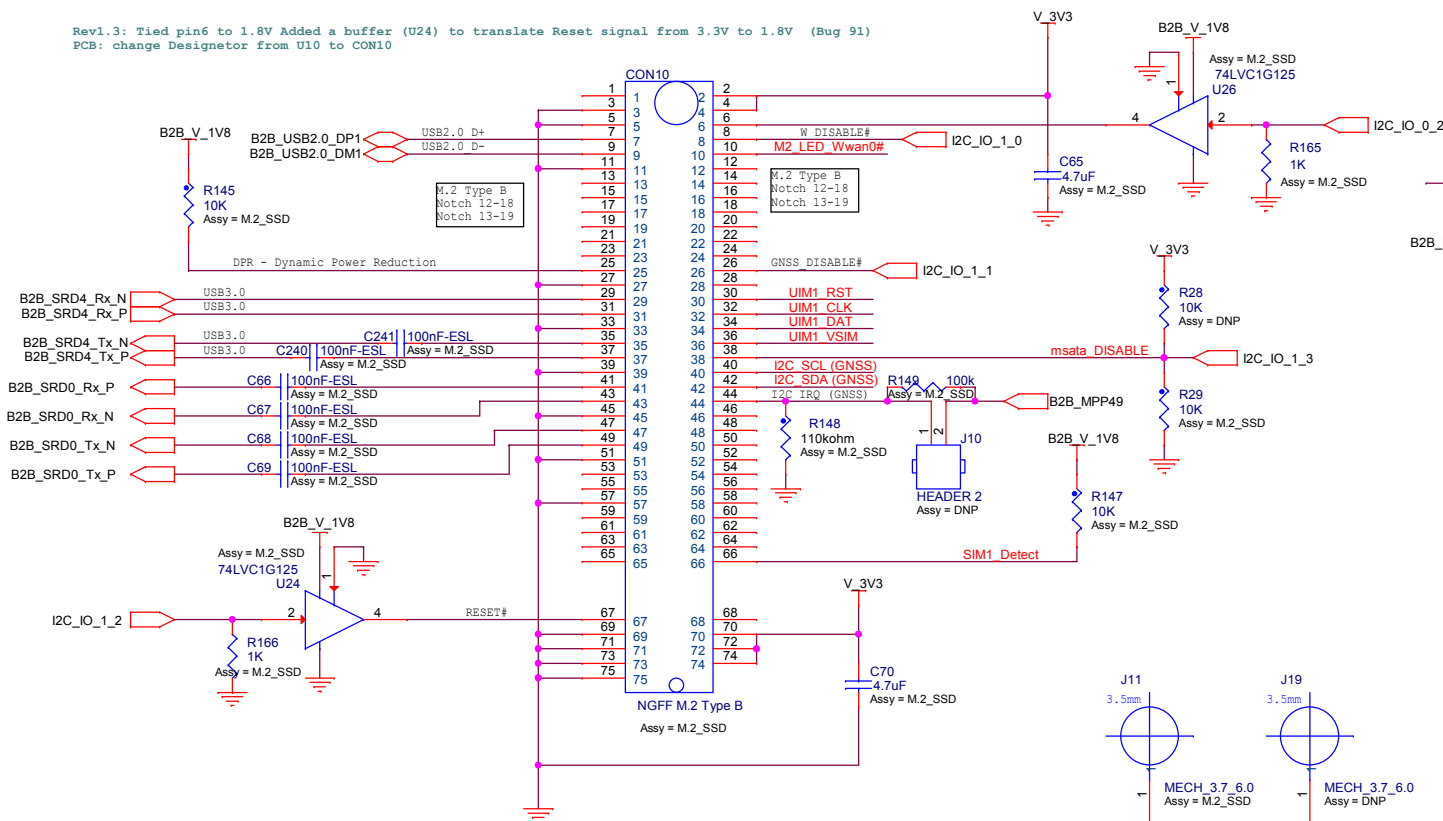
miniPCIe0



Mechanical holes/spacers for the mPCIe module

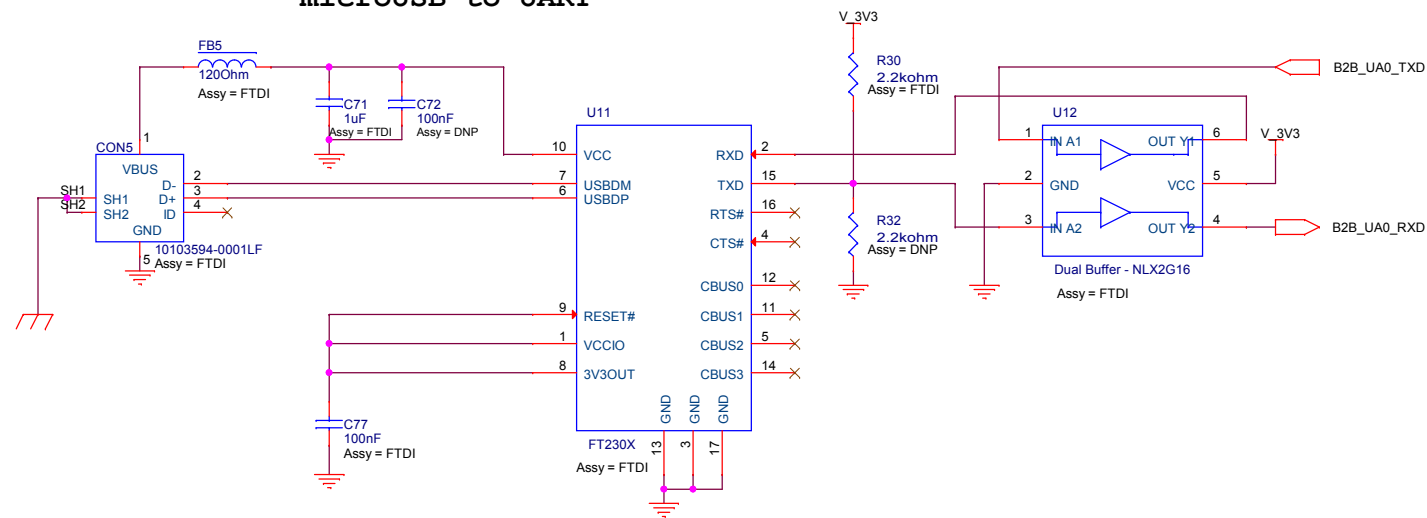


Rev1.3: Tied pin6 to 1.8V Added a buffer (U24) to translate Reset signal from 3.3V to 1.8V (Bug 91)
PCB: change Designator from U10 to CON10

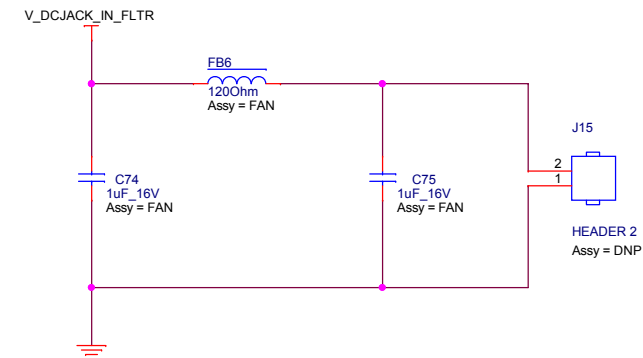


30x42mm modules
22x30mm modules

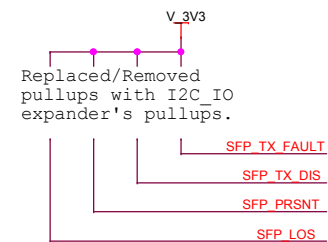
microUSB to UART



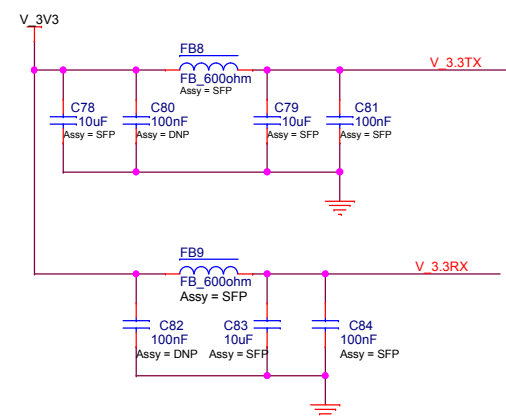
FAN Power



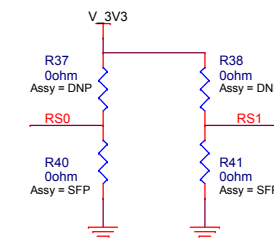
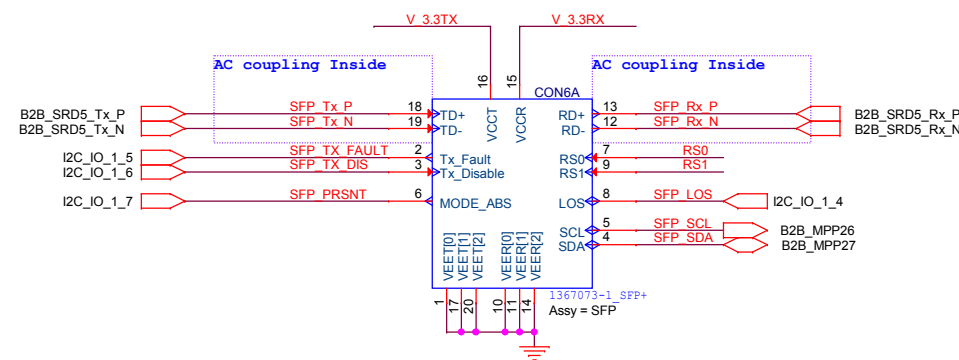
SFP + defines maximum current
withdraw from V_3.3TX and V_3.3RX as
300mA each.



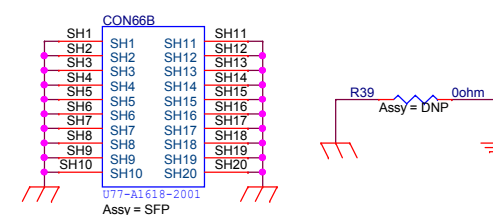
SFP TRANSCEIVER Power

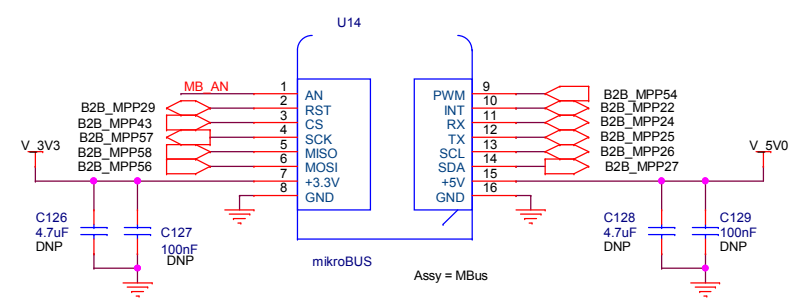
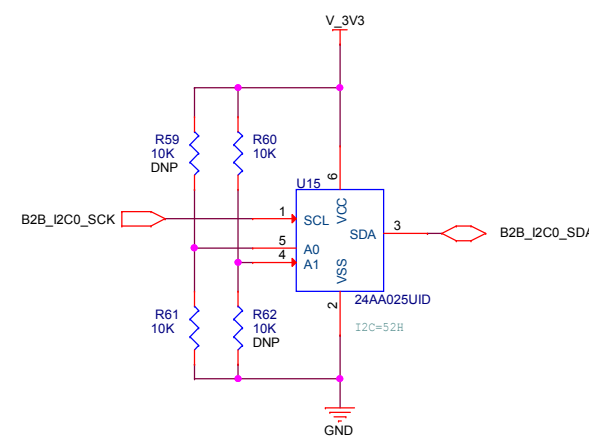
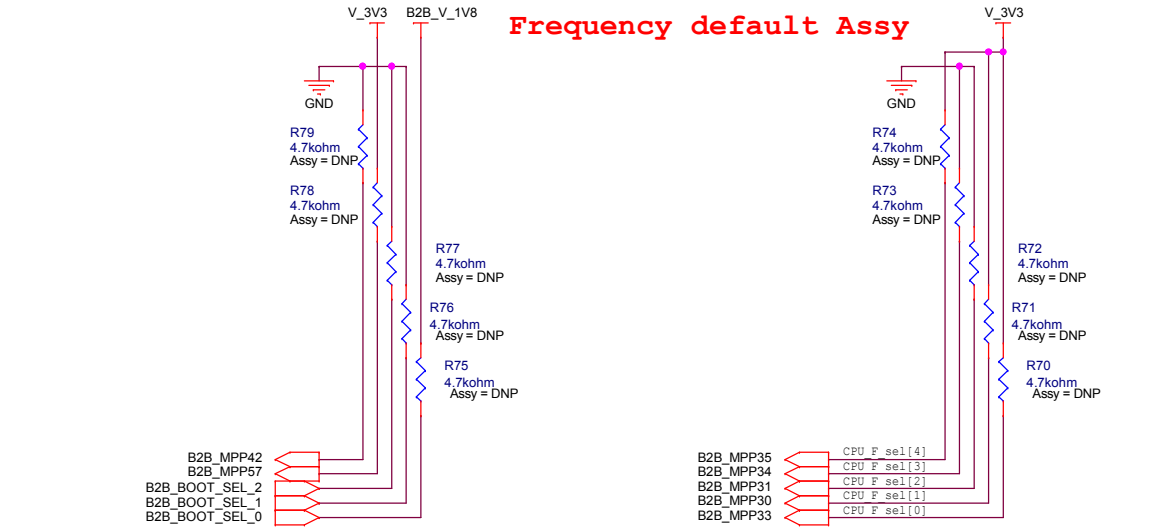
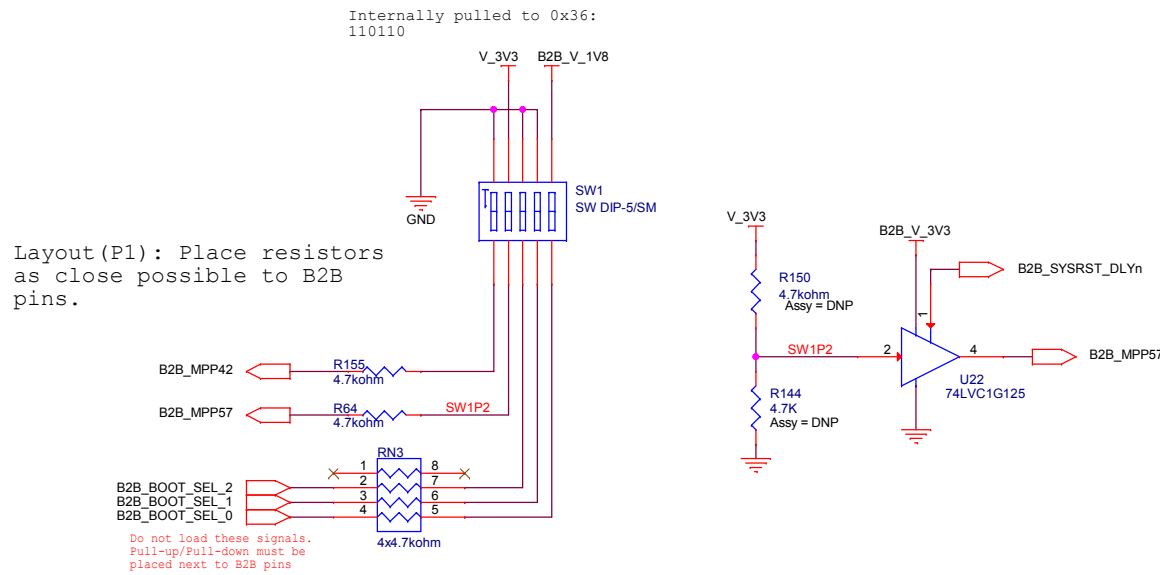


SFP TRANSCEIVER



SFP Shield

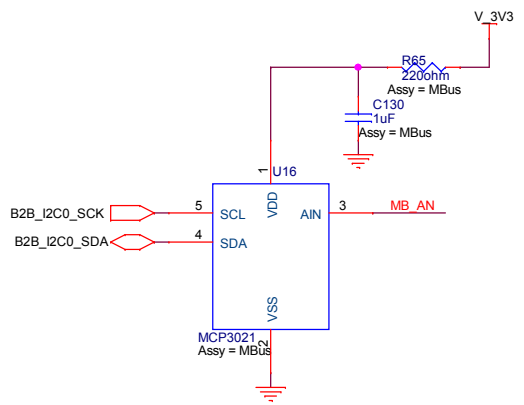




PART NO.	XX	X	/XX	
Device	Address Options	Temperature Range	Package	
Device: MCP3021T: 10-Bit 2-Wire Serial A/D Converter (Tape and Reel)				
Temperature Range: E = -40°C to +125°C				
Address Options:	XX	A2	A1	A0
A0	=	0	0	0
A1	=	0	0	1
A2	=	0	1	0
A3	=	0	1	1
A4	=	1	0	0
A5 *	=	1	0	1
A6	=	1	1	0
A7	=	1	1	1
* Default option. Contact Microchip factory for other address options				
Package:	OT = SOT-23, 5-lead (Tape and Reel)			

Examples:

- a) MCP3021A0T-E/OT: Extended, A0 Address, Tape and Reel
- b) MCP3021A1T-E/OT: Extended, A1 Address, Tape and Reel
- c) MCP3021A2T-E/OT: Extended, A2 Address, Tape and Reel
- d) MCP3021A3T-E/OT: Extended, A3 Address, Tape and Reel
- e) MCP3021A4T-E/OT: Extended, A4 Address, Tape and Reel
- f) MCP3021A5T-E/OT: Extended, A5 Address, Tape and Reel
- g) MCP3021A6T-E/OT: Extended, A6 Address, Tape and Reel
- h) MCP3021A7T-IE/OT: Extended, A7 Address, Tape and Reel



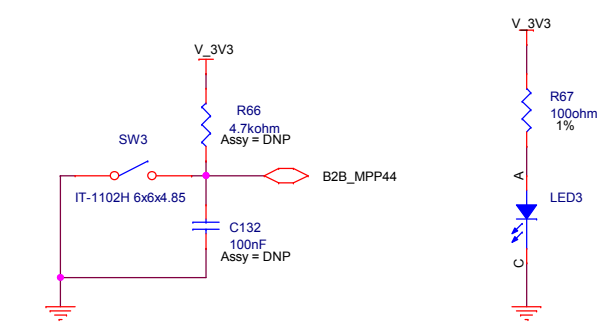
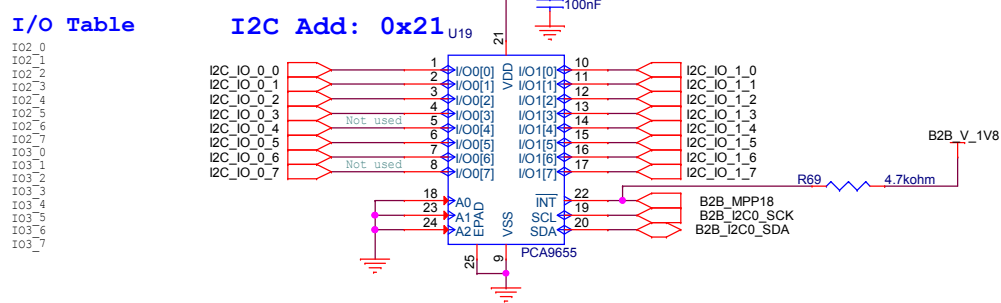
PCA9555

16-bit I2C and SMBus I/O port with interrupt

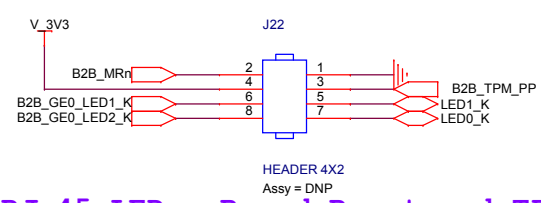
5V tolerant I/Os

I/O port

When an I/O is configured as an input, FETs Q1 and Q2 are off, creating a high impedance input with a weak pull-up to VDD. The input voltage may be raised above VDD to a maximum of 5.5 V



AP Reset Switch (GPIO)



RJ-45 LEDs, Board Reset and TPM_PP

