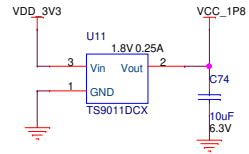
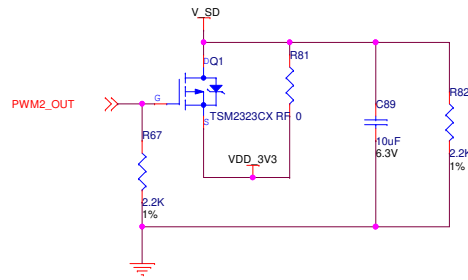


SDIO 3.3v / 1.8v switch circuitry

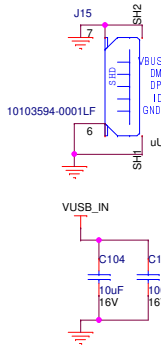


micro SD power on/off

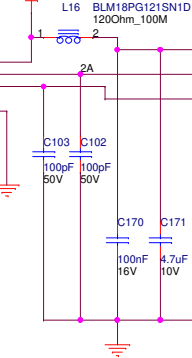


DEBUG/Control PORT

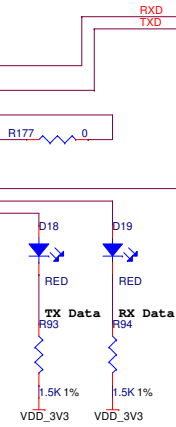
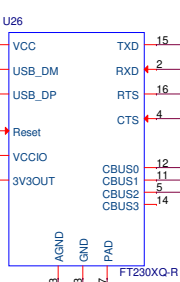
Micro USB Type A/B



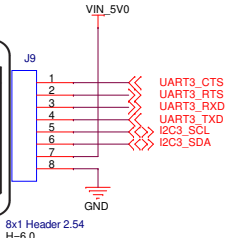
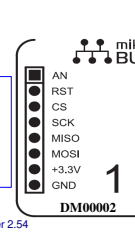
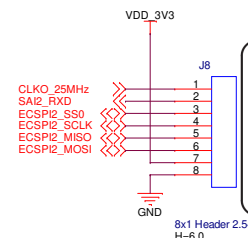
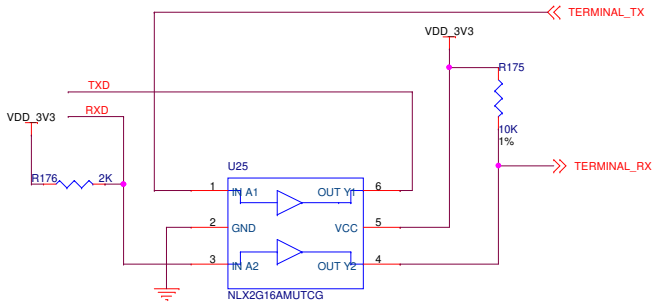
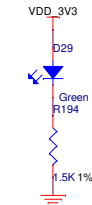
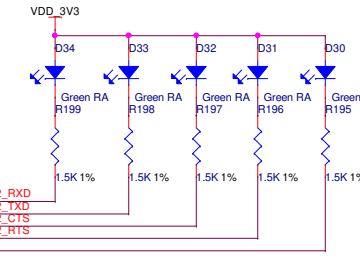
VUSB_IN



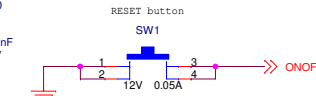
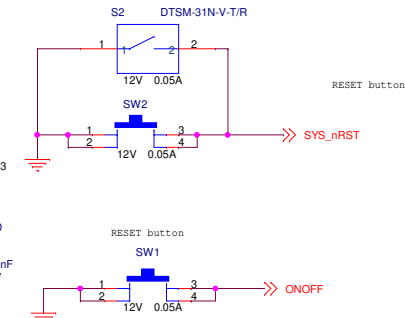
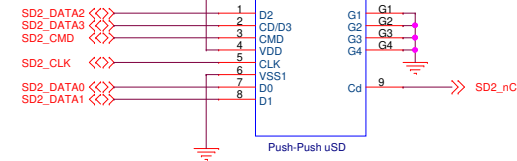
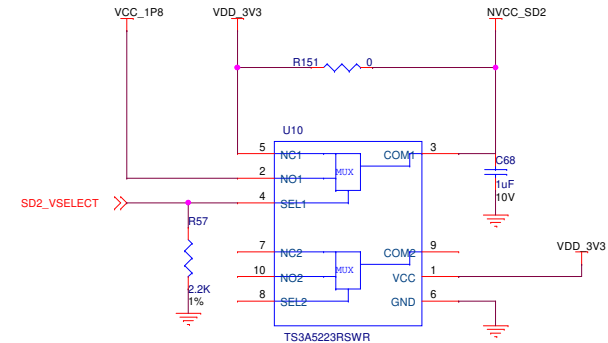
USB to RS-232

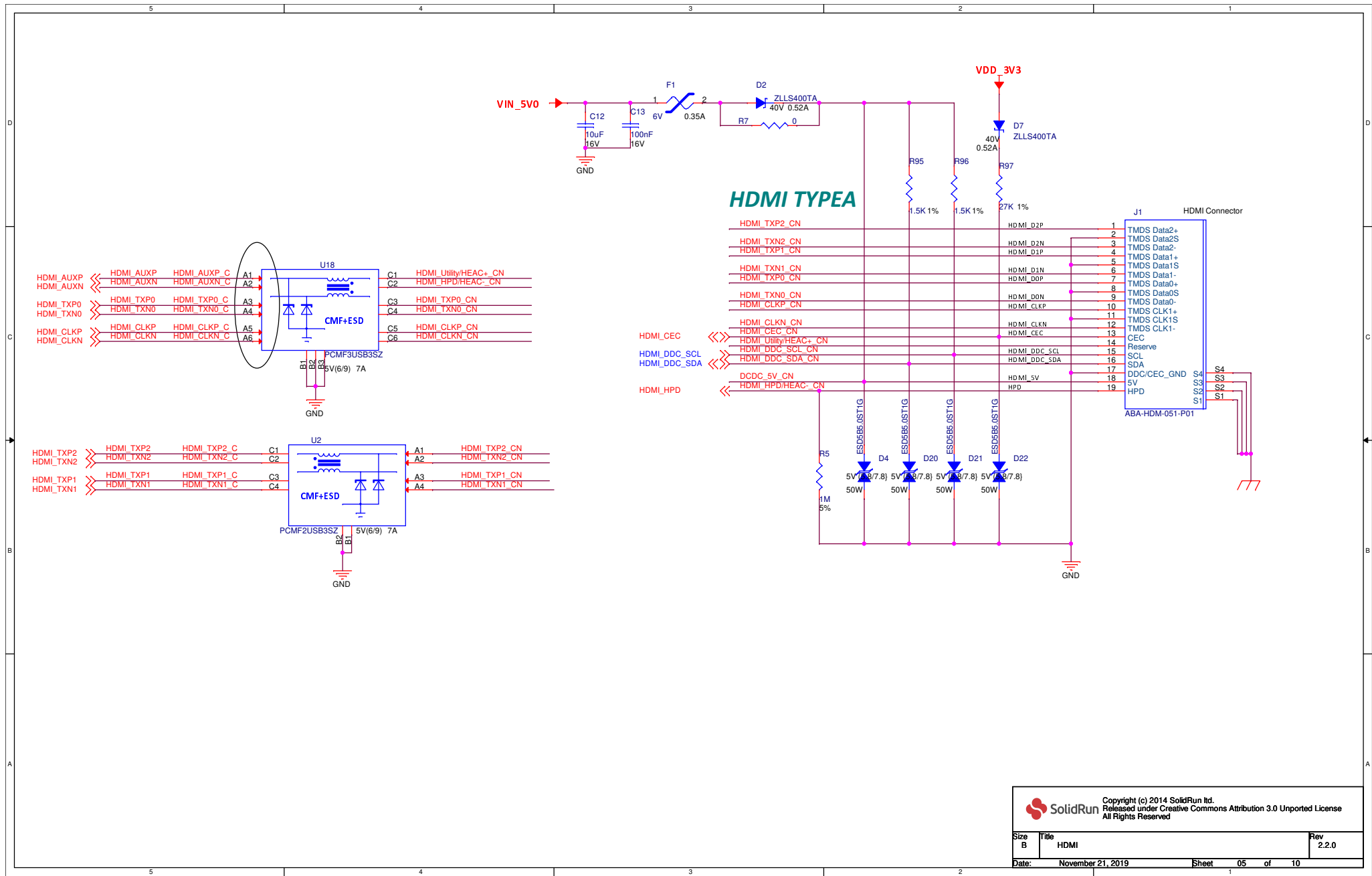


UART2_RXD
UART2_TXD
UART2_CTS
UART2_RTS

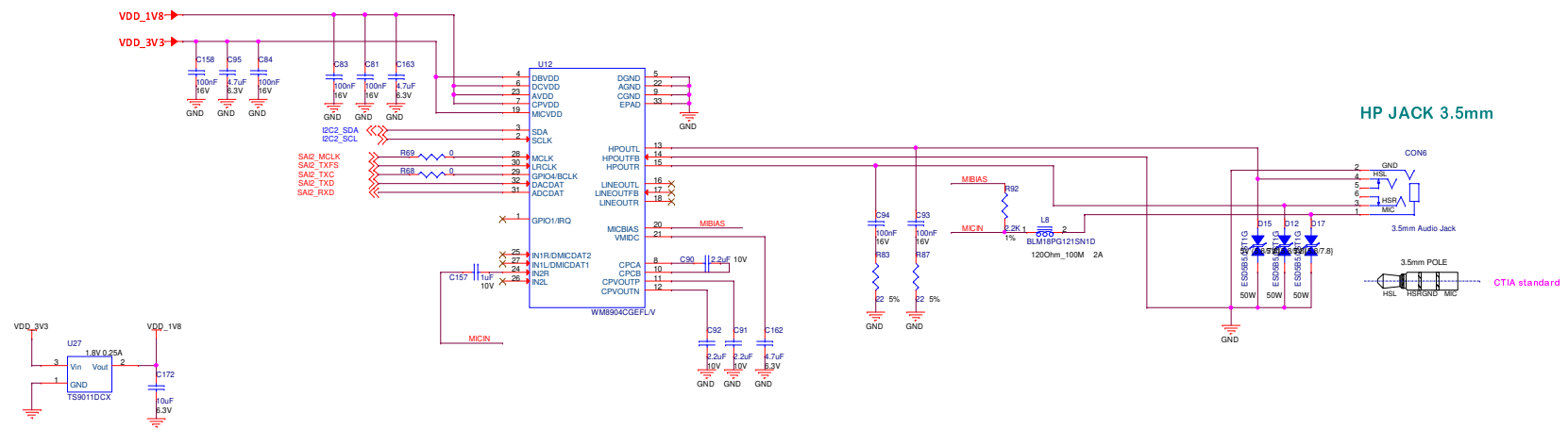


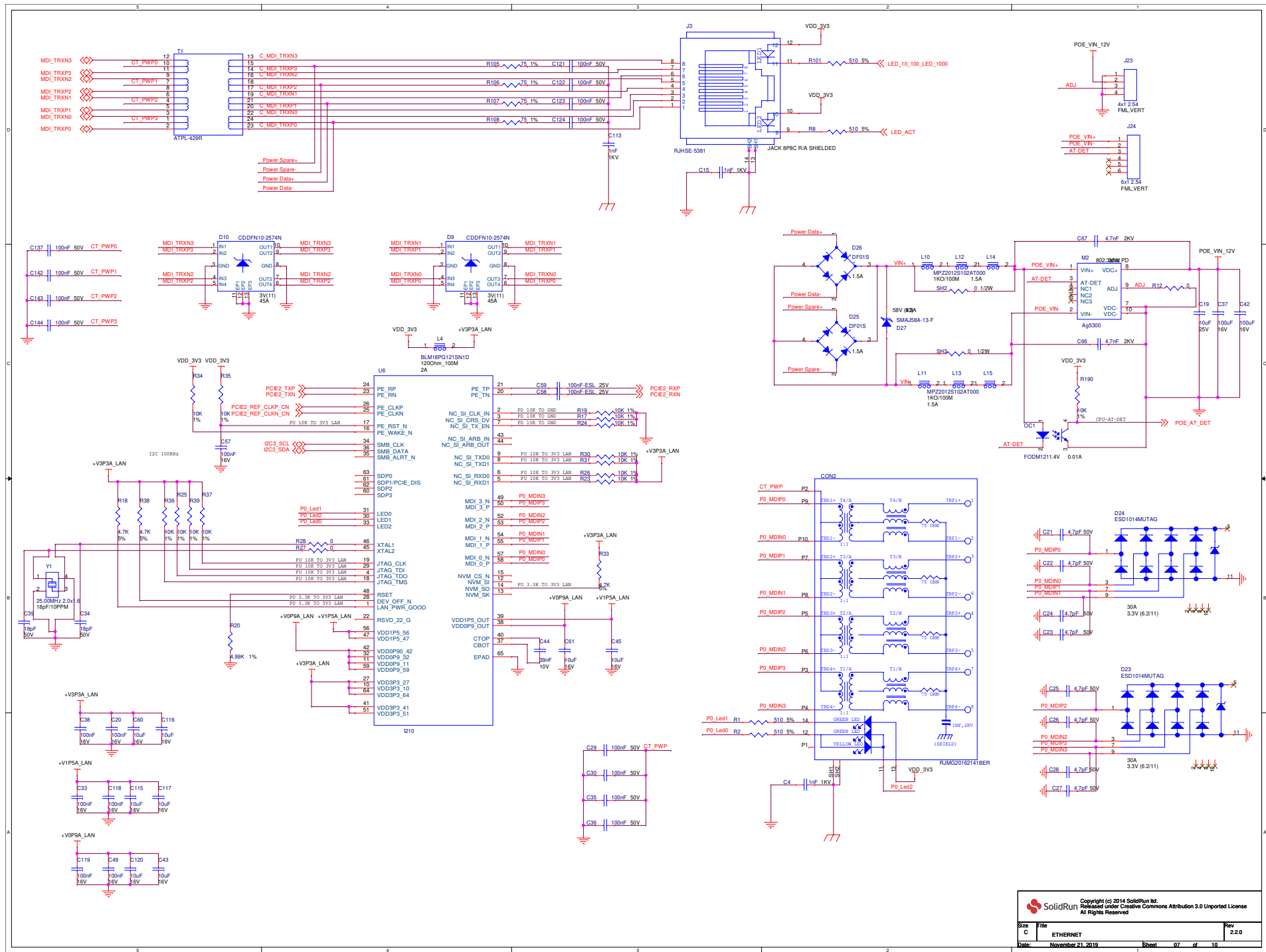
micro SD connectors (assembly option of push-push type and hinged type)





Audio DAC







Rev. 1.0	Prototype Version
Rev. 2.0	<ol style="list-style-type: none"> 1. Change R257 from 0 Ohm to 100K 2. Adding C297 a 4.7uF on USB Debug 3. Change J35 Routing 4. R23 is assembled instead of R25 to support standalone USB HUB mode 5. Connecting VIN_5V0 to DCDC_5V 6. Changing the Power load switch to TPS25942A to support reverse power blocking 7. Changing J5001, J7 and J9 routing to support the new IMX-8 SOM 8. Change the Audio CODEC to WM8904 to support IN/OUT 9. Change Y4 from 27MHz to 25MHz
Rev. 2.1	<ol style="list-style-type: none"> 1. Adding R373 and R374 0 Ohm to pull OVP input down for U40 and U41. <p>Taking U37 and U38 out of the BOM and change revision to 2.1.3</p>
Rev. 2.2	New features product
Rev. 2.3	<ol style="list-style-type: none"> 1. Fixing I2C routing on U14 2. Fixing symbols errors on layout - U8, U9 and CON7 3. Changing OC1 position to enable POE module assembly on print side
Rev. 2.3.1	<ol style="list-style-type: none"> 1. R185 is not assembled



Copyright (c) 2014 SolidRun Ltd.
Released under Creative Commons Attribution 3.0 Unported License
All Rights Reserved

Size A	Title HISTORY	Rev 2.3.1
Date:	December 30, 2019	Sheet 11 of 11