

CN9130 SOM

SolidRun Ltd.

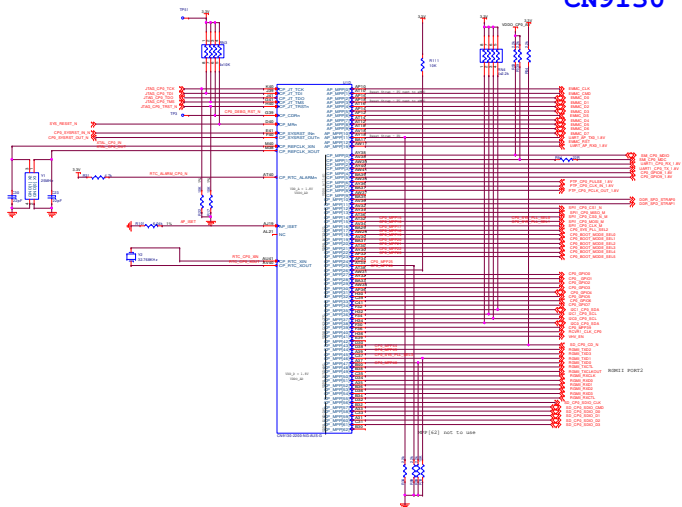
Revision History

Rev#	Date	Owner	Description
1.0	Jan 2021	AlonR	First Release
1.1	June 2021	AlonR	1. Fix DDR bus width B0C 2. Update MPFS to support ROM11 CP_0E2 3. Fix ROM11 1.0V support with CP_VDD0 A 4 D 4. Correct EEPROM address to 0x53 5. changed SPI0SE due to stock shortage 6. changed 1.2V DC-DC due to stock shortage 7. Removed M0J 8. Changed SPI MUX

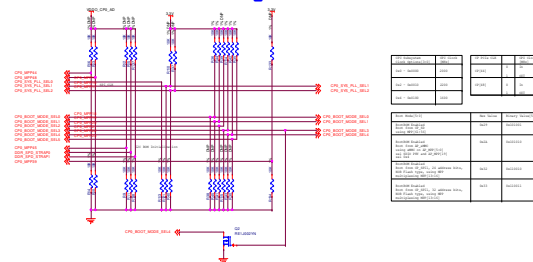
Table of Content

Page#	Page Name	Description
0	Index	
1	B2B-DC-DC Perph	Board to Board connectors, MISC and power blocks
2	DDR	DDR4 46bit BUS
3	Processor	MPFS, CPU Power, GND and SERDESs

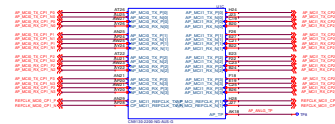
CN9130 CP0 MPPs



CP0 Boot Straps



CN9130 MoCi



CN9130 SERDES

